

Notice of Allowability	Application No.	Applicant(s)	
	10/777,587	BENCIVENGA ET AL.	
	Examiner	Art Unit	
	Terrence R. Willoughby	2836	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to June 27, 2006.
2. ☒ The allowed claim(s) is/are 17.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

RESONSONS FOR ALLOWANCE

1. Examiner acknowledges a submission of the amendment filed on June 27, 2006. Claims 1-16 have been cancelled and new claim 17 is added. It also included remarks/arguments.
2. The following is an examiner's statement of reasons for allowance:
3. Regarding claim 17, Carpenter, Jr. (US 4,389,695) discloses a data surge protection module for protecting data communication circuits form voltage surges (col. 1, ll. 63-68, col. 2, ll. 1-6), comprising: a housing (Fig. 4, 31); a printed circuit board within the housing (Fig. 2, 21); a first diode bridge (Fig. 1, 16A-16D) connected to a first signal line (Fig. 1, 19A) and a first signal output line (Fig. 1, 18A) and a second signal line (Fig. 1, 19B) and a second output line (Fig. 1, 18B) to direct electrical pulses between said first signal line and said second output line (col. 2, ll. 67-68, col. 3, ll. 1-2); and a second diode bridge (Fig. 1, 16E-16H) coupled to the first diode bridge and a avalanche diode (Fig. 1, 17A, 17B) coupled across the first and second diode bridges.

Peng (US 5,717,561) discloses a sharing arrangement for surge protection circuitry (Fig. 1), comprising a plurality of transmission line-pairs (10-18) utilizing a single multi-terminal protective network (34) coupled to individual lines (52-56) of the transmission line-pairs by a plurality of full-wave diode bridge circuits (48) and resettable fuse elements (62,64).

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Latuszkin et al. (US 5,706,160) discloses a surge protector module (Fig. 1-4) for protection telecommunications equipment from power and transient surges including a housing (12), a printed circuit board (26), and a plurality of circuit surge protectors devices (28a-28d) mounted on the printed circuit board.

However, the prior art of record failed to teach six conductor electrical voltage surge protection module having a plurality of diode steering bridges coupled to a **single** silicon avalanche diode (SAD), wherein the silicon avalanche diode is coupled to the output of all three diode bridges.

As a result for claim 17 the allowability resides, at least in part, in the above-described limitations, and in combination with the other limitations recited in the claims, which has not been disclosed in or rendered obvious by the Prior Art.

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

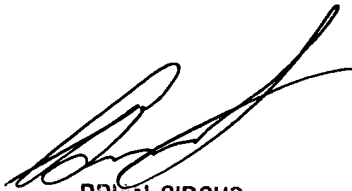
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Terrence R. Willoughby whose telephone number is 571-272-2725. The examiner can normally be reached on 8-5pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2800 ext. 36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TRW
8/31/06



BRIAN SIRCUS
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